

G O D W I N G R U B E R

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j1052 U.S. PTO

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Jc973 U.S. PTO
09/975630
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October 9, 2001

VIA EXPRESS MAIL

EL858166615US

Assistant Commissioner for Patents
Washington, D.C. 20231

Re: Patent Application For
**CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION
FUNCTIONS OF CIRCUITS AND LEADFRAMES INTO THE CHIP
SURFACE**

Attorney Docket No. TI-31678

Our File: 50000.2156

Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Non-Provisional Application/Fee Authorization/transmittal Form ;
- (2) Declaration, and Power of Attorney;
- (3) Recordation Form Cover Sheet with Assignment;
- (4) Specifications and Formal Drawings; and
- (5) Postcards.

Please charge **Deposit Account No. 20-0668** in the amount of the total fees set forth. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to **Deposit Account No. 20-0668**.

Please file the application and return the date-stamped postcard to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Sincerely

Gary C. Honeycutt
Gary C. Honeycutt
Reg. No. 20,250

GCH/ecc

Enclosures

cc: Lawrence Bassuk, Texas Instruments Incorporated

NON-PROVISIONAL APPLICATION FOR U. S. PATENT UNDER 37 CFR 1.53(b)
TRANSMITTAL FORM

Attorney Docket No. TI-31678

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

Transmitted herewith for filing is the
patent application of:

Inventor(s): Taylor R. Efland

"EXPRESS MAILING" Mailing Label No. EL868166615US.
Date of Deposit: **October 9, 2001**. I hereby certify that this paper is
being deposited with the U.S. Postal Service Express Mail
Post Office to Addressee Service under 37 CFR 1.10 on the
date shown above and is addressed to the Assistant Commissioner
for Patents, Washington, D.C. 20231.

Emily C. Chantry

Date

For: **Circuit Structure Integrating the Power Distribution Functions of Circuits and Leadframes into the Chip Surface**

Enclosed are:

- 1 sheet of formal drawings and 27 pages of Specification (including Abstract)
X A Declaration/Power of Attorney
X Assignment with form PTO 1595

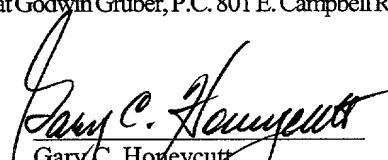
Please amend the specification by inserting before the first line the sentence:
This application claims priority under 35 USC § 119 (e) (1) of provisional application number
60/240,452, filed October 13, 2000.

FEE CALCULATION					FEE
	NUMBER		NUMBER EXTRA	RATE	BASIC FEE \$ 740.00
Total Claims	23	-20 =	3	X \$18 =	\$ 54.00
Independent Claims	3	- 3 =	0	X \$84 =	\$ 0.00
Total Filing Fee					\$794.00

Please charge **Deposit Account No. 20-0668** in the amount of the Total Fees set forth. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to **Deposit Account No. 20-0668**. **This form is submitted in triplicate.**

All correspondence related to this application may be addressed to the undersigned at Godwin Gruber, P.C. 801 E. Campbell Rd. Suite 655, RICHARDSON, TX 75081.

Date: October 9, 2001


Gary C. Honeycutt
Registration No. 20,250